IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.:	10/766,611
Applicants:	SWANSON, Travis et al.
Filed:	28 January 2004
Titled:	Providing Memory Test Patterns For DLL Calibration
Art Unit:	2117
Examiner:	CHUNG, Phung M.
Atty. Docket No.:	PAT001096-000 (013721-0023-999)

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office action of 14 May 2009, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.